

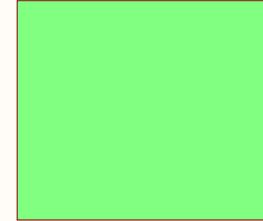
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# TITLE PAGE

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Top.SchDoc



ECO  
ECO LIST.SchDoc



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## IMPORTANT NOTES ABOUT THIS SCHEMATIC

DESIGN NOTE: Example text for the design note to show the note inside the colored box.

1) DESIGN NOTES in grey are information notes.

DESIGN NOTE: Example text for the design note to show the note inside the colored box.

2) DESIGN NOTES in red are critical, and must be understood and followed.

✘ 3) A red X indicates suppression of error checking on a pin/net. Commonly suppressed errors include: single-pin net, no driving source, etc.

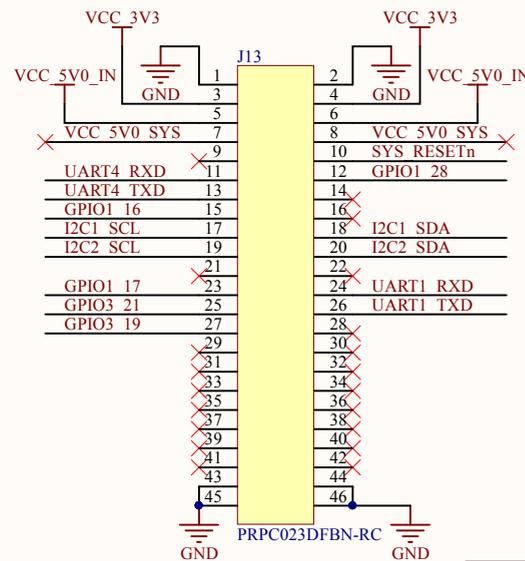
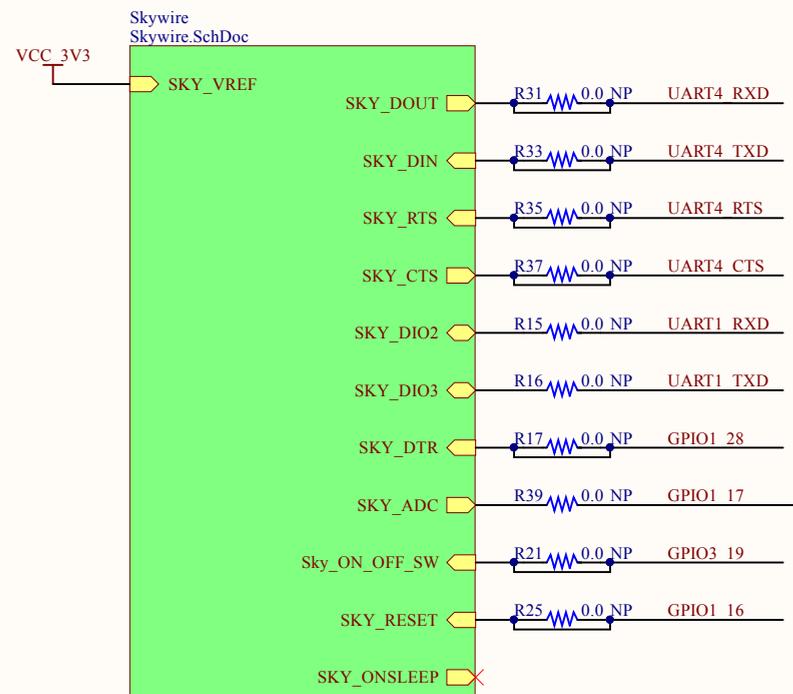
4) All unique components in this schematic should have a manufacturer's part number displayed; exceptions to this rule are commodity passives such as resistors and capacitors.

5) Finally, population vs. non-population intent is indicated by adding "NP" next to the part. All parts with "NP" next to the part are intended to be unplaced during assembly.

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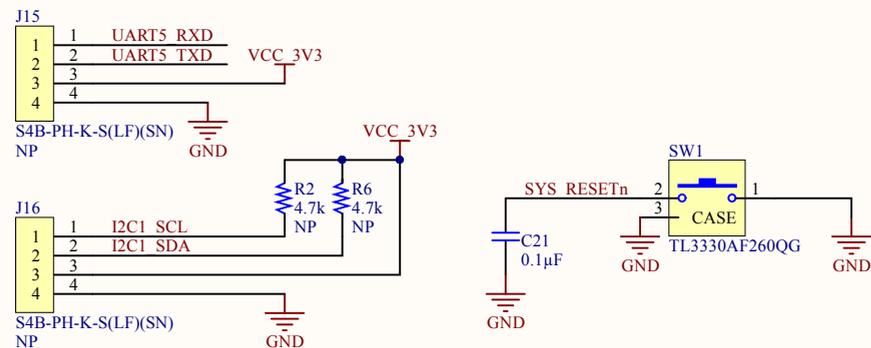
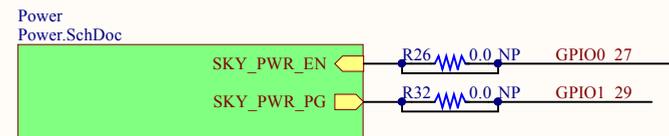
		www.nimbelink.com	
Title: TITLE		MINNEAPOLIS	
Project: Skywire Beaglebone Cape Lite	Size: B	Sheet 1	of 5
Number: 20057	Rev: B	Modified: 2/21/2017	1:25:45 PM
Prepared for: Nimbelink			

DESIGN NOTE: VCC\_5V0\_IN is the raw 5V input to the beaglebone PMIC from the beaglebones 5V barrel jack. The VCC\_5V0\_SYS is the 5V0 output from the Beaglebones PMIC.

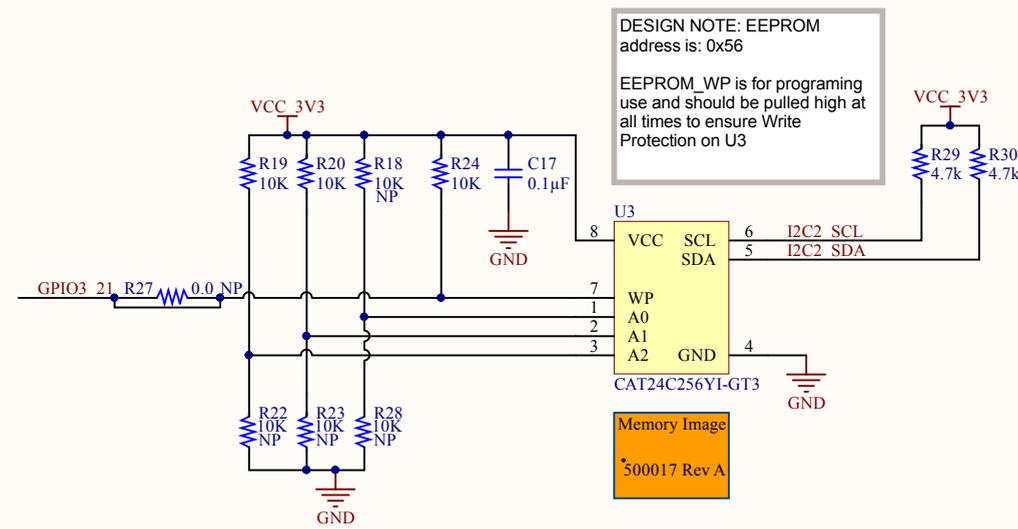


DESIGN NOTE: 0 ohm resistors R27, R26, R32, R31, R33, R35, R37, R17, R21, and R25 have a short through them to allow the 0 ohm resistors not to be placed. If users need to reroute a signal cut the trace inbetween the resistor pads, this will allow for the signal to be easily reconnected if needed.

DESIGN NOTE: J10 is connected to the Beaglebone P8 connector.  
J13 is connected to the Beaglebone P9 connector.



PCB  
20057 Rev B



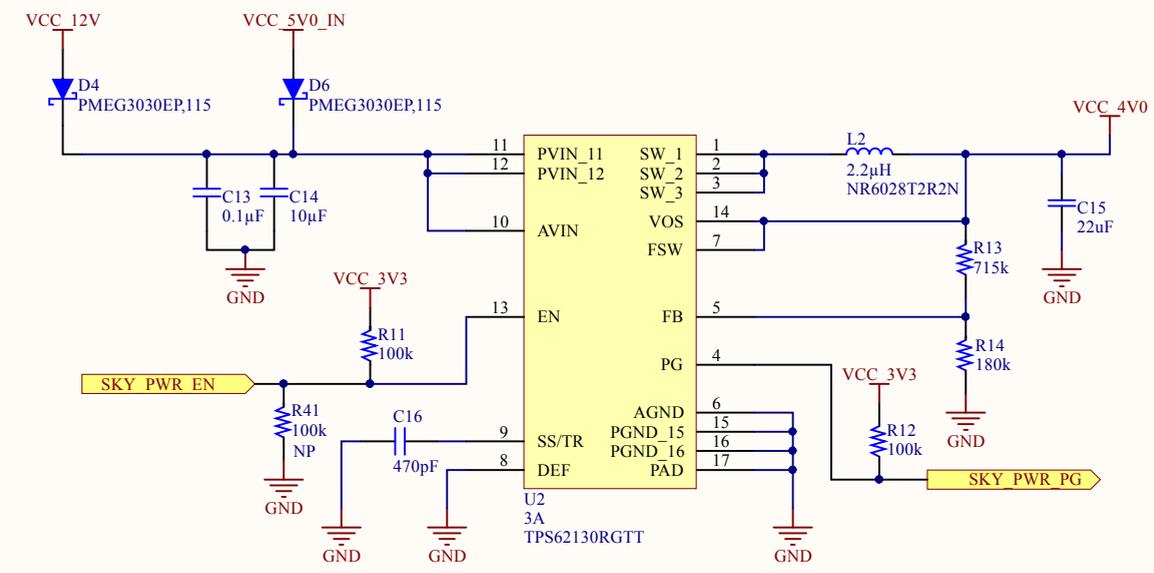
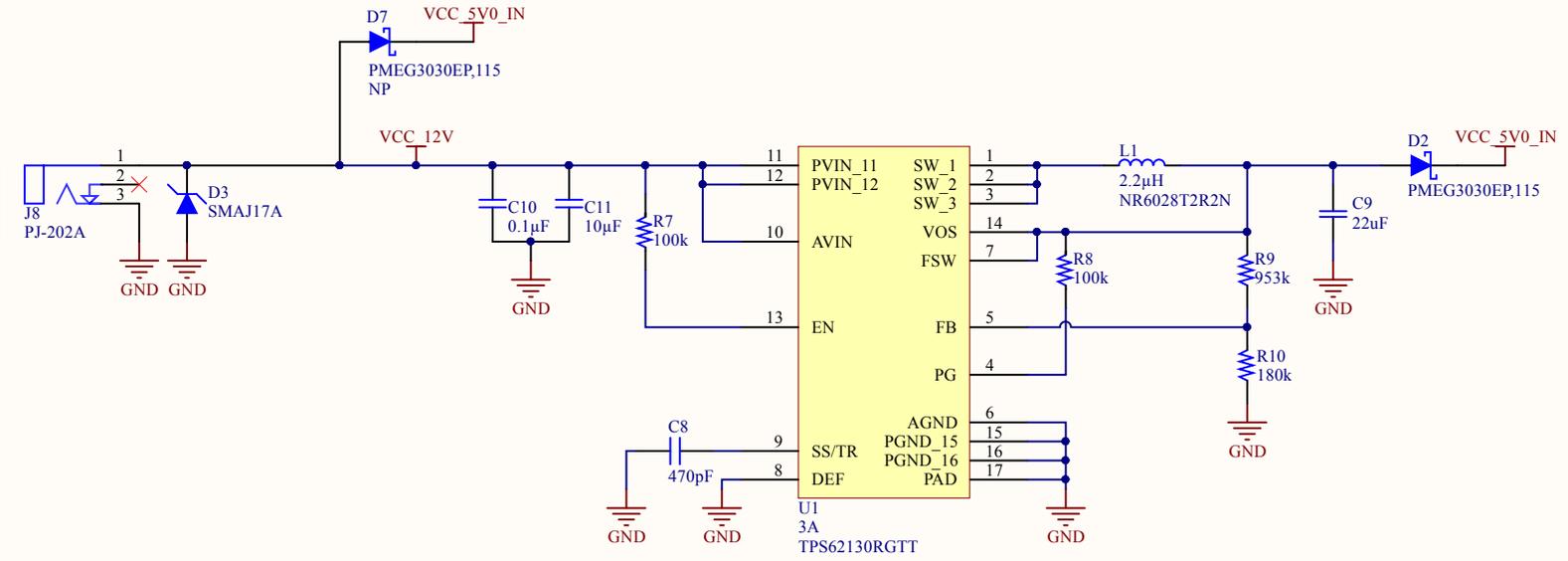
DESIGN NOTE: EEPROM address is: 0x56  
EEPROM\_WP is for programming use and should be pulled high at all times to ensure Write Protection on U3



# POWER

DESIGN NOTE: VIN input range on J8: 6V-15V

DESIGN NOTE: D7 footprint placed to allow no-pop of U1 for 5V system input. If D7 is populated do not use 12V input.



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